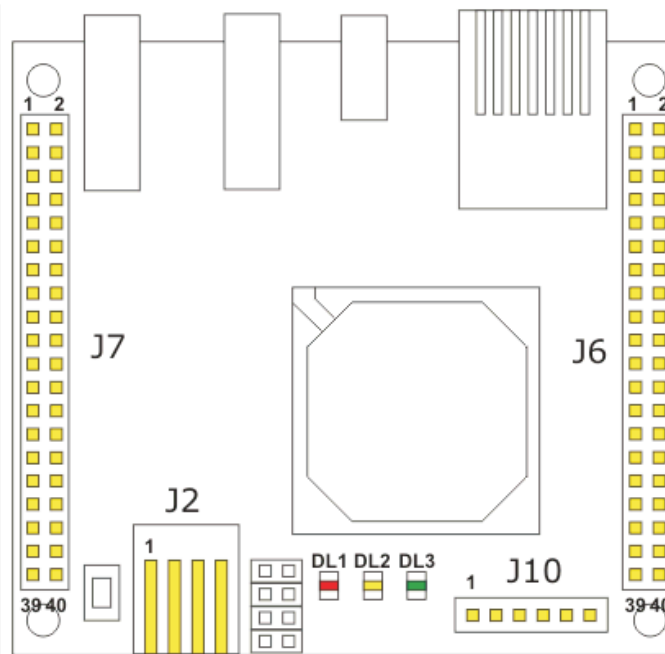


FOX Board LX - Pinout - Top view

J7				
Pin #	I/O Line	Default function		Def. Dir
1		GND	GND	
2		GND	GND	
3	IOG22	IOG22	Input/Output line G22	I
4	IOG23	IOG23	Input/Output line G23	I
5	IOG20	IOG20	Input/Output line G20	I
6	IOG21	IOG21	Input/Output line G21	I
7	IOG18	IOG18	Input/Output line G18	I
8	IOG19	IOG19	Input/Output line G19	I
9	IOG16	IOG16	Input/Output line G16	I
10	IOG17	IOG17	Input/Output line G17	I
11	OG26		VMO line on USB2	
12	OG29	OG29	Output line G29	O
13	OG25	OG25	Output line G25	O
14	IG25		VM line on USB2	
15	OG28	OG28	Output line G28	O
16	OG27		VPO line on USB2	
17	IG28		SPEED line on USB2	
18	IG27		RCV line on USB2	
19	IG26		VP line on USB2	
20	IG29		OE line on USB2	
21	IOG24	IOG24	Input/Output line G24	I
22	IOG0	IOG0	Input/Output line G0 (I2C_RES)(1)	O
23			SPEED line on USB1	
24			RCV line on USB1	
25			OE line on USB1	
26			VP line on USB1	
27	RESET		Reset line (Out)	O
28	INTA		INTA (Out)	O
29	+5V		Vcc (5V)	
30	IRQ		Interrupt request line	I
31	PA7	DCD2	Data carrier detect /dev/ttyS2	I
32	PA6	DSR2	Data set ready /dev/ttyS2	I
33	PA5	RI2	Ring indicator /dev/ttyS2	I
34	PA4	DTR2	Data terminal ready /dev/ttyS2	O
35	PA3	DL1	Connected to DL1 Red led	O
36	PA2	DL2	Connected to DL2 Yellow led	O
37	PA1	SW1	Connected to SW1	I
38	PA0	PA0	Input/Output line A0	I
39		+3.3V	Vcc (3.3V)	
40		+3.3V	Vcc (3.3V)	



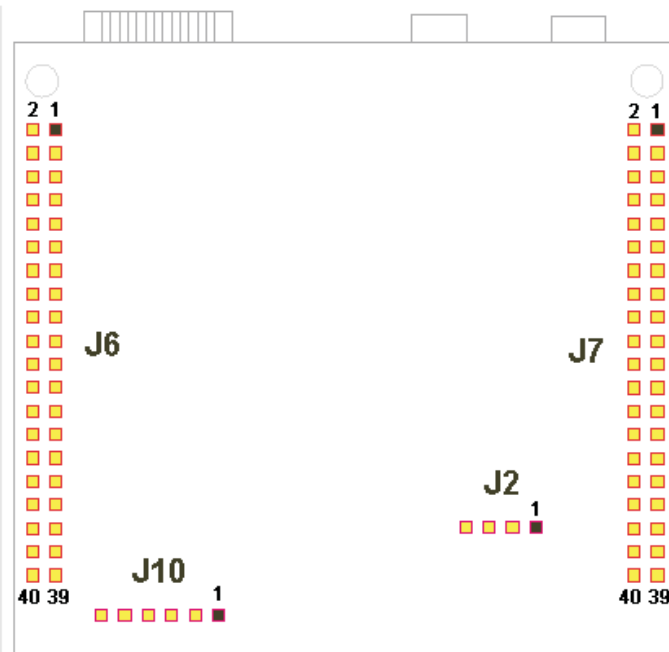
J2				
Pin #	I/O Line	Default function		Def. Dir
1		N.C.		
2		GND	GND	
3		GND	GND	
4		+5V	Vcc (5V)	

J10				
Pin #	I/O Line	Default function		Def. Dir
1		+3.3V	Vcc (3.3V)	
2		RTS0	Request to send /dev/ttyS0	O
3		TXD0	Transmit data /dev/ttyS0	O
4		RXD0	Receive data /dev/ttyS0	I
5		CTS0	Clear to send/dev/ttyS0	I
6		GND	GND	

J6				
Pin #	I/O Line	Default function		Def. Dir
1		+3.3V	Vcc (3.3V)	
2		+3.3V	Vcc (3.3V)	
3	OG31	RTS3	Request to send /dev/ttyS3	O
4	IG30	RXD3	Receive data /dev/ttyS3	I
5	OG30	TXD3	Transmit data /dev/ttyS3	O
6	IG31	CTS3	Clear to send/dev/ttyS3	I
7	OG7	RTS2	Request to send /dev/ttyS2	O
8	IG6	RXD2	Receive data /dev/ttyS2	I
9	OG6	TXD2	Transmit data /dev/ttyS2	O
10	IG7	CTS2	Clear to send/dev/ttyS2	I
11		NMI	Non maskable Interrupt	I
12		+5V	Vcc (5V)	
13	IOG9	IOG9	Input/Output line G9	I
14	IOG8	IOG8	Input/Output line G8	I
15	IOG11	IOG11	Input/Output line G11	I
16	IOG10	IOG10	Input/Output line G10	I
17	IOG13	IOG13	Input/Output line G13	I
18	IOG12	IOG12	Input/Output line G12	I
19	IOG15	IOG15	Input/Output line G15	I
20	IOG14	IOG14	Input/Output line G14	I
21	OG2	OG2	Output line G2 (I2C_RESET)(1)	O
22	OG5	OG5	Output line G5	O
23	OG1	OG1	Output line G1	O
24	IG1	IG1	Input line G1	I
25	OG4	OG4	Output line G4	O
26	OG3	OG3	Output line G3	O
27	IG4	IG4	Input line G4	I
28	IG3	IG3	Input line G3	I
29	IG2	IG2	Input line G2	I
30	IG5	IG5	Input line G5	I
31	PB1	I2C_CLK	IC2 Clock	O
32	PB0	I2C_DATA	IC2 Data	I
33	PB3		VMO line on USB1	
34	PB2		VPO line on USB1	
35	PB5		VM line on USB1	
36	PB4	PB4	Input/Output line B4	I
37	PB7	PB7	Input/Output line B7	O
38	PB6	PB6	Input/Output line B6	O
39		GND	GND	
40		GND	GND	

FOX Board LX - Pinout - Bottom view

J6				
Pin #	I/O Line	Default function		Def. Dir
1		+3.3V	Vcc (3.3V)	
2		+3.3V	Vcc (3.3V)	
3	OG31	RTS3	Request to send /dev/ttyS3	O
4	IG30	RXD3	Receive data /dev/ttyS3	I
5	OG30	TXD3	Transmit data /dev/ttyS3	O
6	IG31	CTS3	Clear to send/dev/ttyS3	I
7	OG7	RTS2	Request to send /dev/ttyS2	O
8	IG6	RXD2	Receive data /dev/ttyS2	I
9	OG6	TXD2	Transmit data /dev/ttyS2	O
10	IG7	CTS2	Clear to send/dev/ttyS2	I
11		NMI	Non maskable Interrupt	I
12		+5V	Vcc (5V)	
13	IOG9	IOG9	Input/Output line G9	I
14	IOG8	IOG8	Input/Output line G8	I
15	IOG11	IOG11	Input/Output line G11	I
16	IOG10	IOG10	Input/Output line G10	I
17	IOG13	IOG13	Input/Output line G13	I
18	IOG12	IOG12	Input/Output line G12	I
19	IOG15	IOG15	Input/Output line G15	I
20	IOG14	IOG14	Input/Output line G14	I
21	OG2	OG2	Output line G2 (I2C_RESET)(1)	O
22	OG5	OG5	Output line G5	O
23	OG1	OG1	Output line G1	O
24	IG1	IG1	Input line G1	I
25	OG4	OG4	Output line G4	O
26	OG3	OG3	Output line G3	O
27	IG4	IG4	Input line G4	I
28	IG3	IG3	Input line G3	I
29	IG2	IG2	Input line G2	I
30	IG5	IG5	Input line G5	I
31	PB1	I2C_CLK	IC2 Clock	O
32	PB0	I2C_DATA	IC2 Data	I
33	PB3		VMO line on USB1	
34	PB2		VPO line on USB1	
35	PB5		VM line on USB1	
36	PB4	PB4	Input/Output line B4	I
37	PB7	PB7	Input/Output line B7	O
38	PB6	PB6	Input/Output line B6	O
39		GND	GND	
40		GND	GND	



J10				
Pin #	I/O Line	Default function		Def. Dir
1		+3.3V	Vcc (3.3V)	
2		RTS0	Request to send /dev/ttyS0	O
3		TXD0	Transmit data /dev/ttyS0	O
4		RXD0	Receive data /dev/ttyS0	I
5		CTS0	Clear to send/dev/ttyS0	I
6		GND	GND	

J2				
Pin #	I/O Line	Default function		Def. Dir
1		N.C.		
2		GND	GND	
3		GND	GND	
4		+5V	Vcc (5V)	

J7				
Pin #	I/O Line	Default function		Def. Dir
1		GND	GND	
2		GND	GND	
3	IOG22	IOG22	Input/Output line G22	I
4	IOG23	IOG23	Input/Output line G23	I
5	IOG20	IOG20	Input/Output line G20	I
6	IOG21	IOG21	Input/Output line G21	I
7	IOG18	IOG18	Input/Output line G18	I
8	IOG19	IOG19	Input/Output line G19	I
9	IOG16	IOG16	Input/Output line G16	I
10	IOG17	IOG17	Input/Output line G17	I
11	OG26		VMO line on USB2	
12	OG29	OG29	Output line G29	O
13	OG25	OG25	Output line G25	O
14	IG25		VM line on USB2	
15	OG28	OG28	Output line G28	O
16	OG27		VPO line on USB2	
17	IG28		SPEED line on USB2	
18	IG27		RCV line on USB2	
19	IG26		VP line on USB2	
20	IG29		OE line on USB2	
21	IOG24	IOG24	Input/Output line G24	I
22	IOG0	IOG0	Input/Output line G0 (I2C_RES)(1)	O
23			SPEED line on USB1	
24			RCV line on USB1	
25			OE line on USB1	
26			VP line on USB1	
27	RESET		Reset line (Out)	O
28	INTA		INTA (Out)	O
29	+5V		Vcc (5V)	
30	IRQ		Interrupt request line	I
31	PA7	DCD2	Data carrier detect /dev/ttyS2	I
32	PA6	DSR2	Data set ready /dev/ttyS2	I
33	PA5	RI2	Ring indicator /dev/ttyS2	I
34	PA4	DTR2	Data terminal ready /dev/ttyS2	O
35	PA3	DL1	Connected to DL1 Red led	O
36	PA2	DL2	Connected to DL2 Yellow led	O
37	PA1	SW1	Connected to SW1	I
38	PA0	PA0	Input/Output line A0	I
39		+3.3V	Vcc (3.3V)	
40		+3.3V	Vcc (3.3V)	